

BEST AVAILABLE COPY

FIGURE 1  
PRIOR ART CACHE

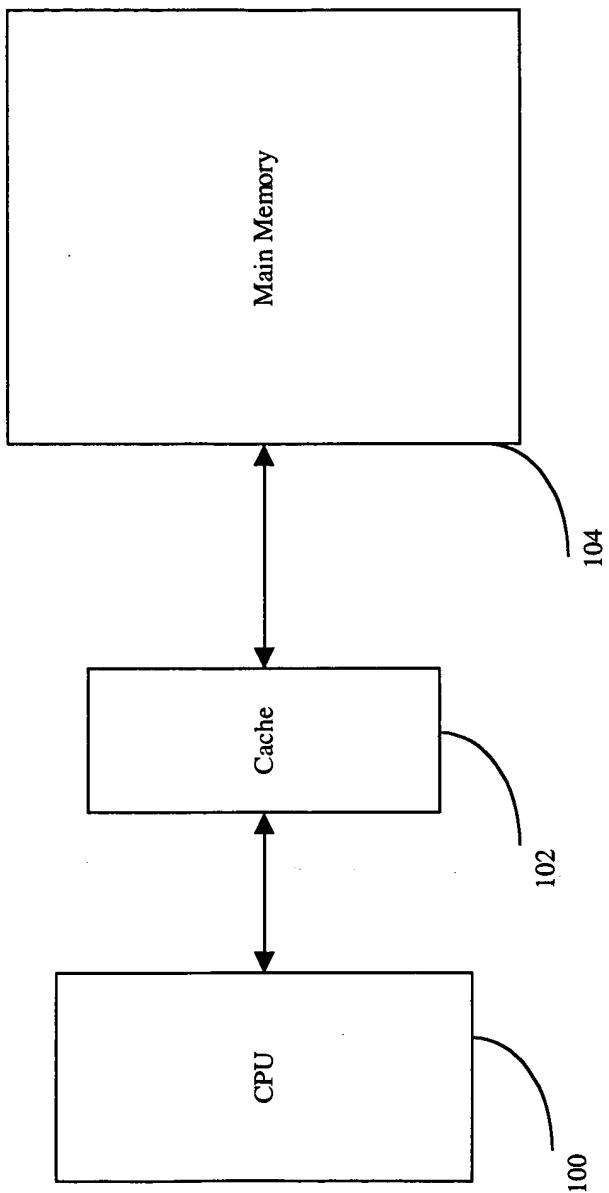
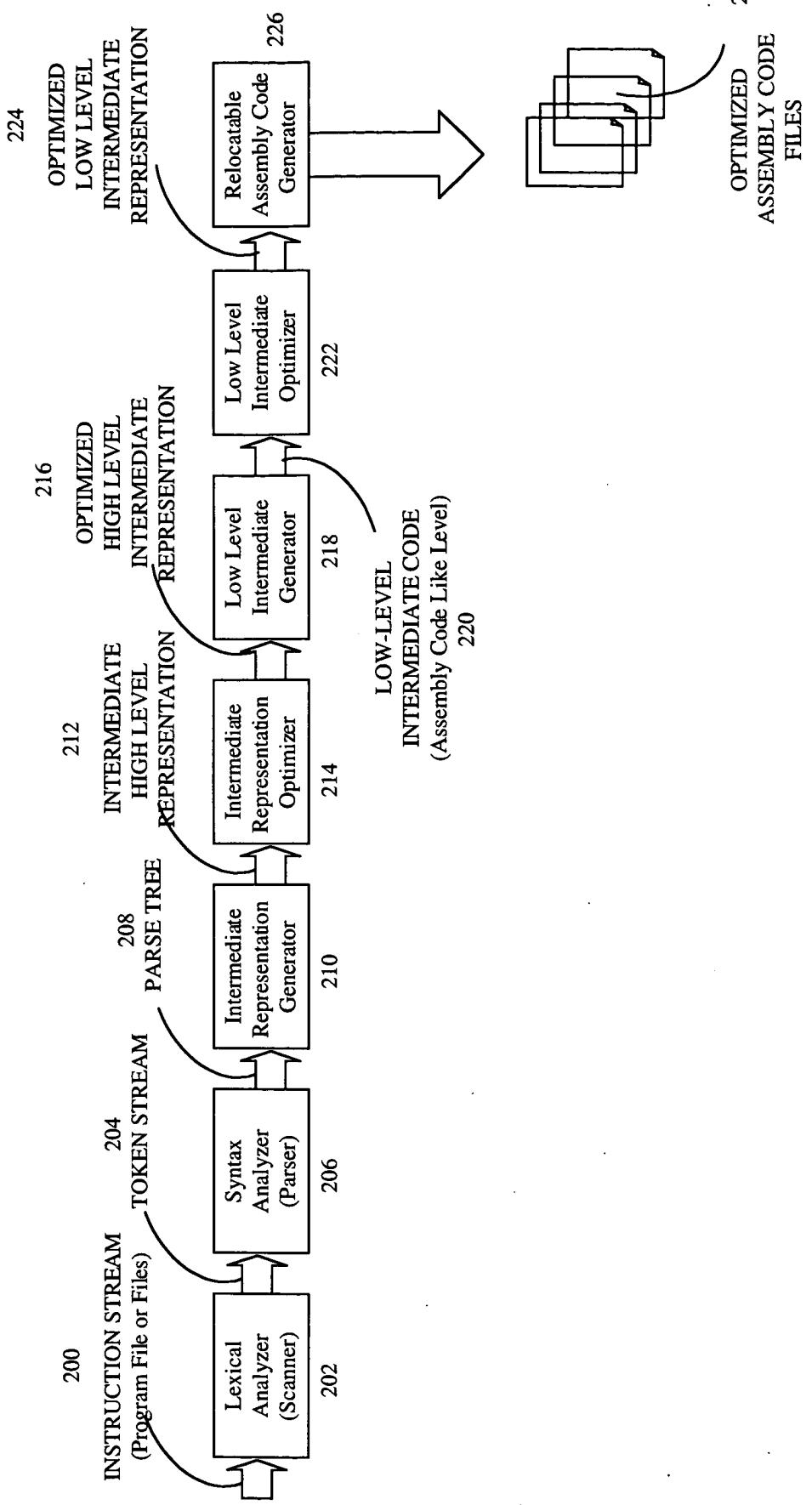


FIGURE 2  
TYPICAL COMPILER



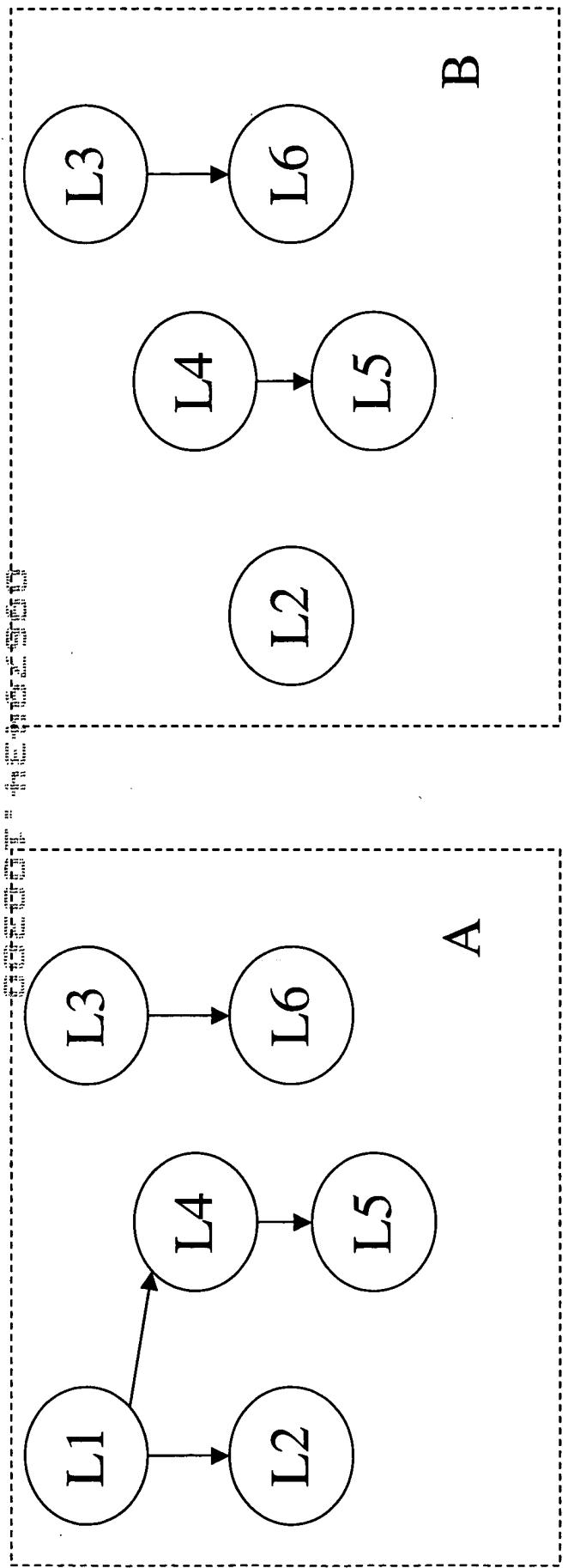


FIGURE 3: Directed Acyclic Graphs  
According To The Present Invention

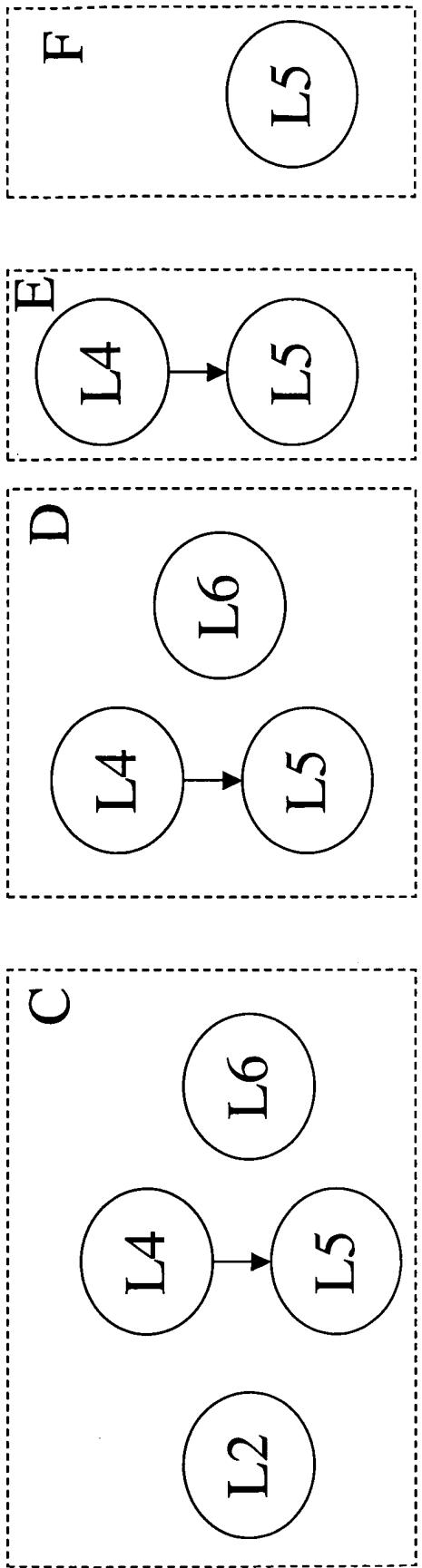


FIGURE 4  
SYSTEM WITH  
COMPILER ACCORDING  
TO THE CURRENT INVENTION

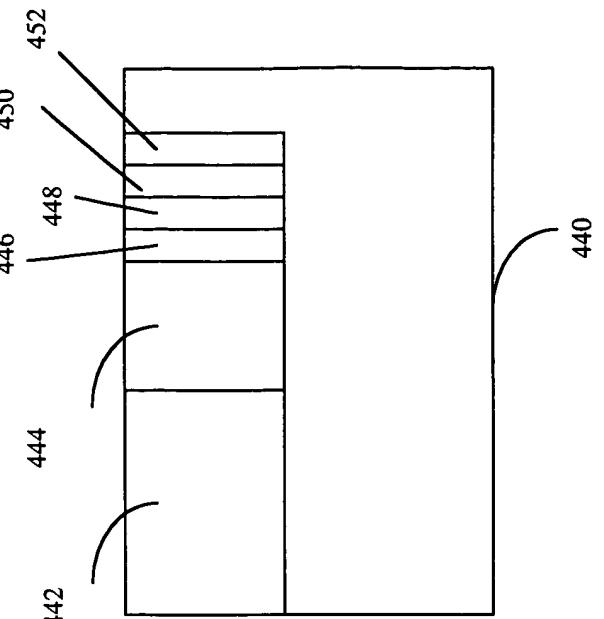
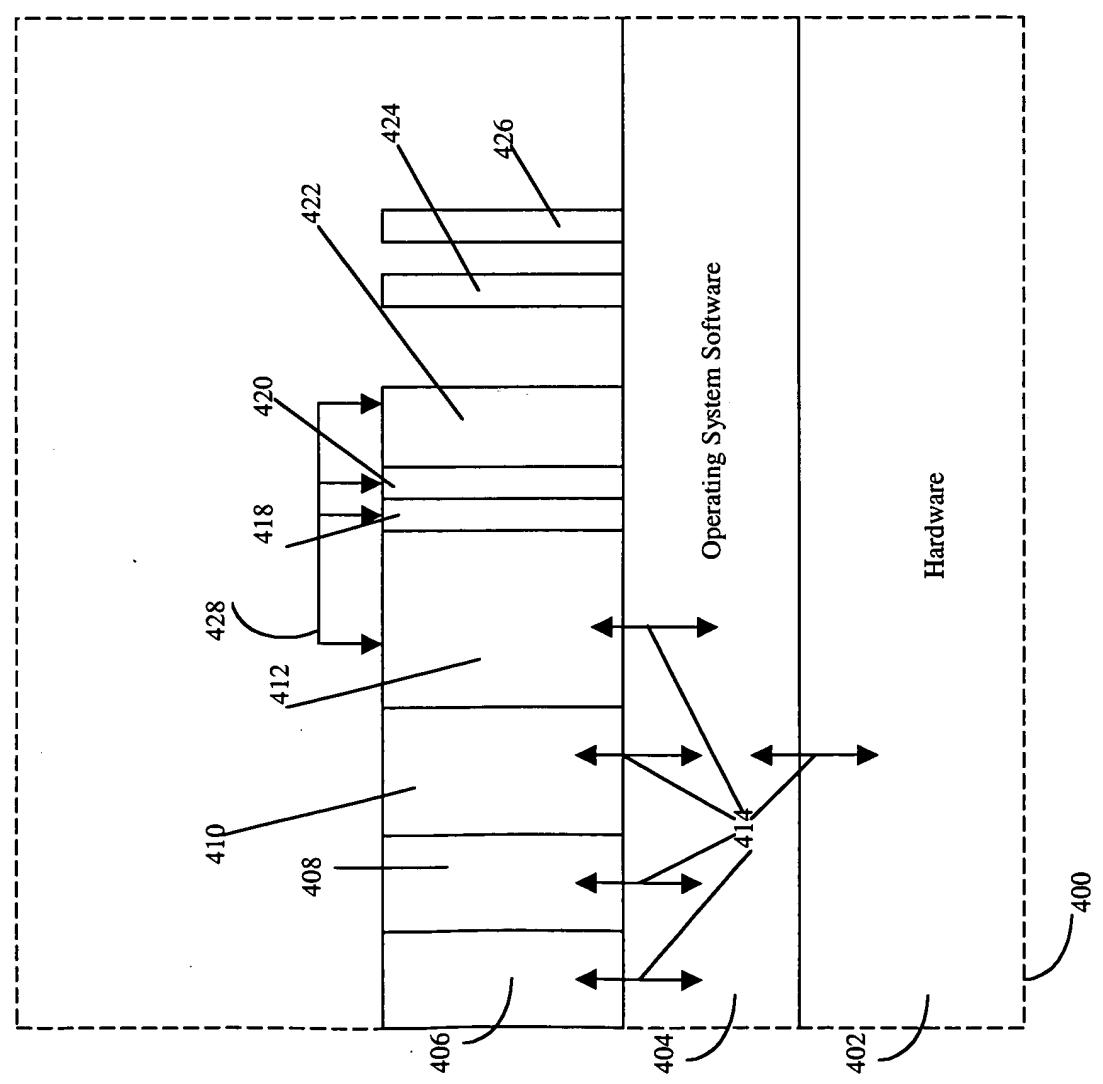
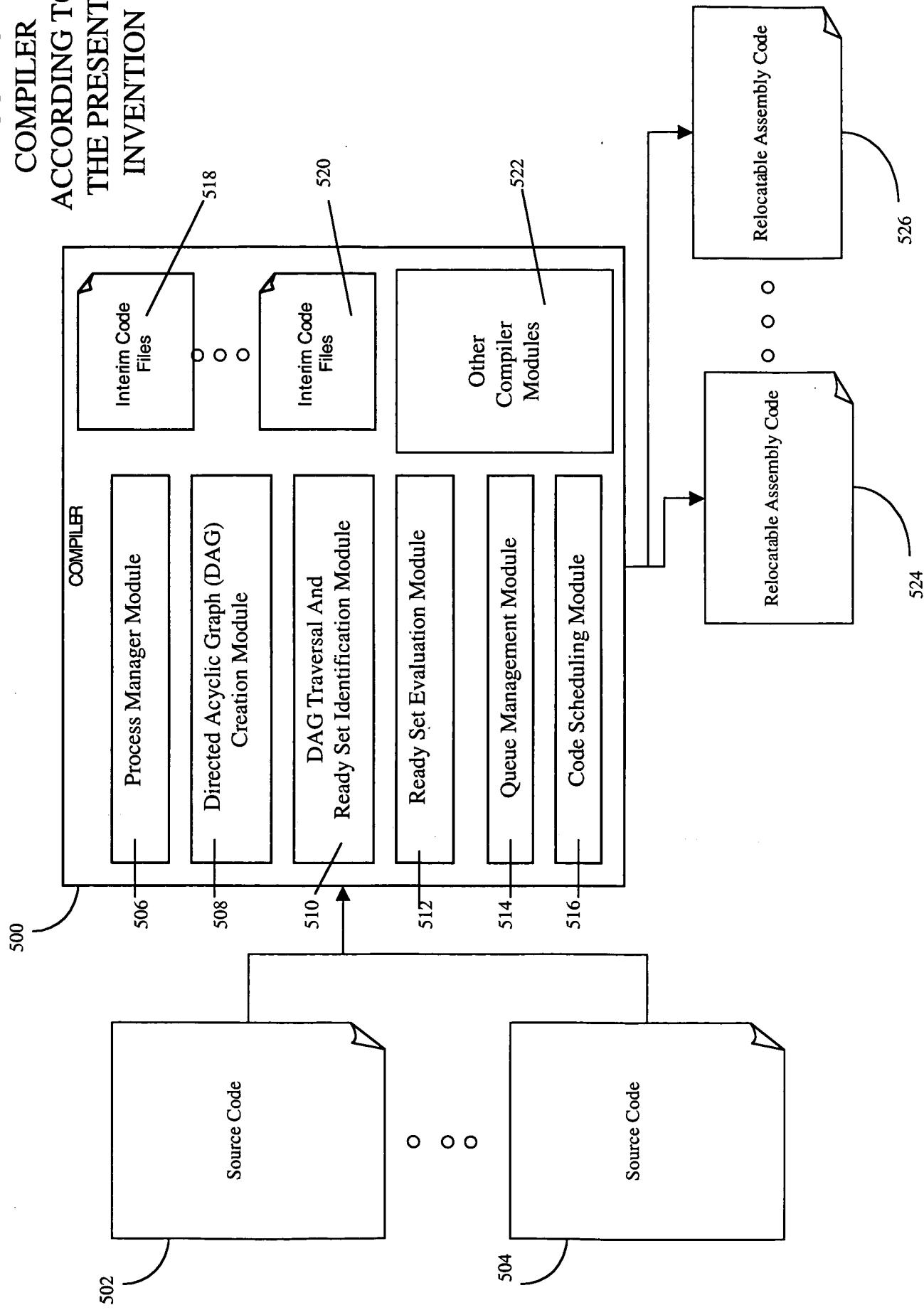


FIGURE 5  
COMPILER  
ACCORDING TO  
THE PRESENT  
INVENTION



**FIGURE 6**  
**MEMORY INSTRUCTION**  
**SCHEDULING**  
**METHOD**

